

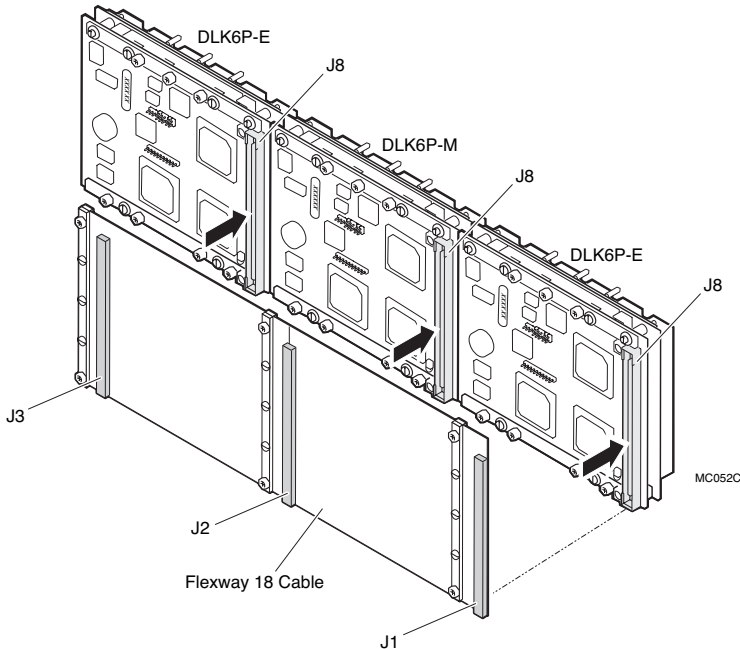
DLK18P Interlink

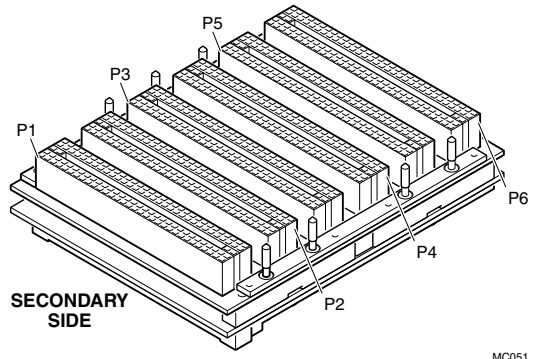
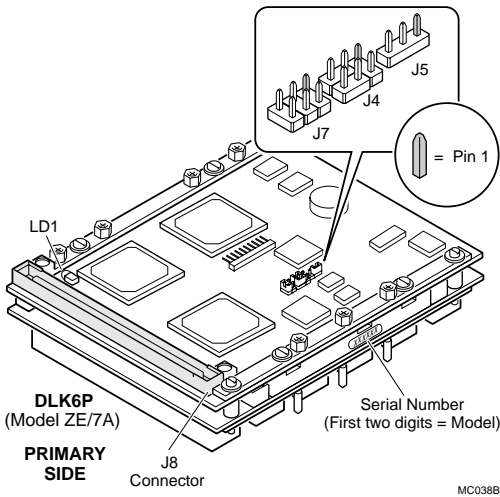
VME RACE++ 18-Slot Dual-Port Motherboard Interlink

DLK18P Interlink Overview

The DLK18P is an assembly of three DLK6P boards, connected by a Flexway 18 cable. User assembly required. Each DLK6P is a dual 6-port RACE++ interlink module that provides multi-port links between resources on the RACEway. It consists of three 8-port crossbars with six dual-port RACEway slots and an expansion connector to connect to other DLK6P devices. The DLK18P plugs into the backplane pins of eighteen VME 5-row P2 connectors.

Layout





Note: The DLK18P is shipped unassembled because assembly of the DLK18P components is performed as they are installed on the VME backplane. Refer to the *DLK12P / DLK18P Interlink Installation Instructions* shipped with the DLK18P.

Indicators

Indicator LD1 lights if any of the three crossbar ASICs registers a system error.

Switches

None.

DLK18P Interlink Specifications

Physical Dimensions

Dimensions (mm): 366.8 x 94.9 (14.4" x 3.74")

Weight (gm): 1168 (42 oz)

Slot-to-Slot spacing (mm): 20.3 (0.8")

Electrical Specifications

Input voltage (Vdc \pm .25): 5.0

Input current in amps: 1.8A typical, 3.9A max

Power dissipation (@ 5.0 Vdc): 9W typical, 19.5W max

Commercial (Model ZE):

Operating temperature range (°C): 0 to 40

Storage temperature range (°C): -40 to 85

Cooling requirements: natural convection

Relative humidity (% noncondensing): 10 to 90

Altitude (ft above sea level): 0 to 10,000

Rugged RACE Level 1 (Model 7A, Part # DLK6P-TVH):

Operating temperature range (°C): -25 to 55

Storage temperature range (°C): -55 to 85

Cooling requirements: natural convection

Relative humidity (% noncondensing): 5 to 95

Altitude (ft above sea level): 0 to 30,000 (operating) and 0 to 50,000 (storage)

Rugged RACE Level 2 (Model 7A, Part # DLK6P-T2VH):

Operating temperature range (°C): -45 to 70

Storage temperature range (°C): -55 to 125

Cooling requirements: natural convection

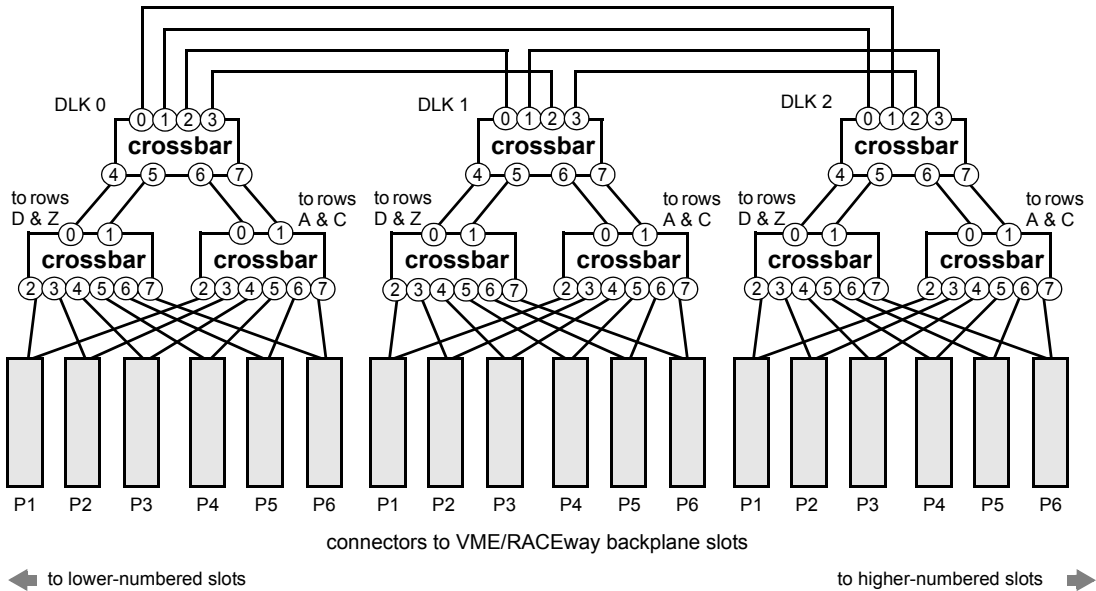
Relative humidity (% noncondensing): 5 to 95

Altitude (ft above sea level): 0 to 30,000 (operating) and 0 to 100,000 (storage)

DLK18P Interlink Installation

For complete installation instructions, refer to the *DLK12P / DLK18P Interlink Installation Instructions* shipped with the DLK18P.

DLK18P Interlink Topology



DLK18P Interlink Configuration

The following sections provide configuration file directives used for this specific device. For general information about creating configuration files and the meaning of the configuration directives, see the *Mercury System Manager's Guide*.

Board Directive

```
board DLK18P user-assigned-name
```

where:

```
user-assigned-name
```

A name assigned by the user.

Connect Directive

```
connect user-assigned-name1.connector1 user-assigned-name2.connector2
```

where:

user-assigned-name1

user-assigned-name2

Two names assigned by the user in a board directive: one for the DLK18P and the other for the hardware device being connected to.

connector1

connector2

Two connectors, one being a connector on the DLK18P and the other a connector on the hardware device being connected to. The DLK18P connector can be:

RWP2_1P through **RWP2_18P** – VME P2 connectors (160-pin), labeled P1 to P6 on the three DLK6Ps. These connectors are the primary connectors on rows A and C. They are compatible with MCJ6 and MCJ9 boards.

RWP2_1S through **RWP2_18S** – VME P2 connectors (160-pin), labeled P1 to P6 on the three DLK6Ps. These connectors are secondary connectors on rows D and Z. They are compatible only with the MCJx boards.

DLK18P Interlink Diagnostics

Diagnostics tool DMC supports the following test phases and tests on this hardware device. For test descriptions, see the *Mercury System Manager's Guide*.

Additional test phases and tests depend on the hardware configuration. See the *Hardware Device Note* for each motherboard, daughtercard or interlink in the system.

ILK Phase

ILK Probe test

ILK DMA test

DLK18P Interlink Reference

Address Space

Information unavailable at time of publication.

Files

`${MC_ROOT_DIR}/etc/desc/DLK18P.desc`

Hardware description file for this device.

See Also

None.